

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s): ABE et al.

Application No.: ~~10/430,257~~

Filed: July 28, 2003

Title: DIODE AND PRODUCING METHOD  
THEREOF

Attorney Docket No.: 01-474

Group Art Unit: 2814

Examiner: PHAM, Long

Commissioner for Patents  
Arlington VA 22202

March 11, 2004

**RESPONSE TO RESTRICTION/ELECTION REQUIREMENT**

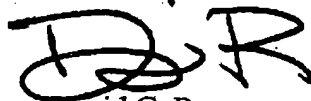
Sir:

In response to a Restriction/Election requirement mailed on February 20, 2004 in connection with the above application, Applicant hereby elects Invention II (claims 1-60, drawn to a semiconductor device) without traverse.

Examination of the present application in view of the above election is respectfully requested.

Please charge any necessary fees to Deposit Account 50-1147.

Respectfully submitted,

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SPECIALIZING IN PATENTS, TRADEMARKS & COPYRIGHTS

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**FACSIMILE TRANSMISSION**

Date: 11/8/2004

Pages: 3

To: Examiner ~~THAM~~ PHAM

From: David Posz

Company: USPTO

Fax No.: 571-273-1714

Subject: Response to Restriction Requirement

Comments:

**PLEASE DELIVER THIS DOCUMENTS TO EXAMINER THAM OF AU 2814**

Best regards,

David Posz  
Posz & Bethards, PLC

\*\*\*\*Notice\*\*\*\*

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akt.

Applicant: ABE et al.

Title: **DIODE AND PRODUCING METHOD THEREOF**

Serial No.: 10/627,729

Filing Date: July 28, 2003

Atty Dkt.: 01-474

.....  
Please acknowledge receipt of the following by date stamping this postcard  
and returning to **Posz & Bethards, PLC**:

- 1-page RESPONSE TO RESTRICTION/ELECTION  
REQUIREMENT



Submission date: March 11, 2004

